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**PATENT**  
**Attorney Docket N<sup>o</sup>: 01-045**  
**Serial N<sup>o</sup>: 09/880,675**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Ghasi R. Agrawal, et al.  
Serial N<sup>o</sup> : 09/880,675  
Filing Date : June 13, 2001  
Examiner :  
Group Art Unit :  
For : **SCAN METHOD FOR BUILT-IN-SELF-REPAIR (BISR)**

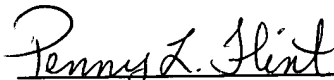
Assistant Commissioner for Patents  
Washington, DC 20231

**RESPONSE TO NOTICE TO FILE  
SUBSTITUTE DRAWINGS (37 C.F.R. 1.84)**

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**CERTIFICATE OF MAILING 37 C.F.R. § 1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service on October 10, 2001, in a First Class envelope, with sufficient postage thereon, addressed to: Assistant Commissioner for Patents, US PTO, Washington, DC 20231.

  
Penny L. Flint

DATED: October 10, 2001

**SUBMISSION OF DRAWINGS**

1. In response to correct submission of informal drawings (37 C.F.R. 1.84(g)) as noted in the *Notice to File Corrected Application Papers* dated August 10, 2001, applicant submits herewith new formal drawings for this application. Number of sheets of drawings submitted: Four (4) sheets consisting of Figures 1 through 4.
2. The two month period of response set in the *Notice to File Corrected Application Papers* expires on October 10, 2001. This submission is on or before the expiration date.

3. While it is believed that no additional extension of time or any other additional fees are necessary, the Commissioner is hereby authorized to grant any needed extension of time and to charge any additional fees which may be required for this Response, or credit any overpayment to Deposit Account No. 19-4882.

Please direct all correspondence to:



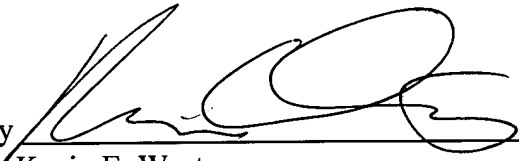
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PATENT TRADEMARK OFFICE

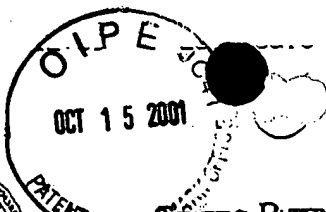
PETER SCOTT  
INTELLECTUAL PROPERTY LAW DEPARTMENT  
LSI LOGIC CORPORATION  
M/S D-106  
1551 MCCARTHY BLVD.  
MILPITAS, CA 95035

DATED: October 10, 2001.

Respectfully submitted,  
LSI Logic Corporation,

By   
Kevin E. West  
Reg. N° 43,983

- ATTACHMENTS:
1. Four (4) sheets of drawings.
  2. Copy of the *Notice to File Corrected Application Papers*.
  3. Copy of this *Response* for accounting purposes.
  4. Return postcard.



UNITED STATES PATENT AND TRADEMARK OFFICE

 COMMISSIONER FOR PATENTS  
 UNITED STATES PATENT AND TRADEMARK OFFICE  
 WASHINGTON, D.C. 20231  
 www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/880,675	06/13/2001	Ghasi R. Agrawal	01-045

CONFIRMATION NO. 1803

## FORMALITIES LETTER



\*OC00000008408689\*

 PETER P. SCOTT  
 INTELLECTUAL PROPERTY LAW DEPARTMENT  
 LSI LOGIC CORPORATION, M/S D-106  
 1551 MCCARTHY BLVD.  
 MILPITAS, CA 95035

Date Mailed: 08/10/2001

## NOTICE TO FILE CORRECTED APPLICATION PAPERS

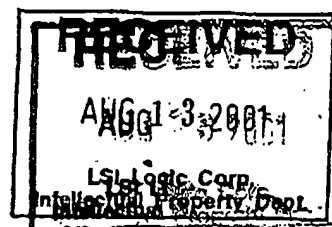
*Filing Date Granted*

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
  - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

A copy of this notice **MUST** be returned with the reply.

 Customer Service Center  
 Initial Patent Examination Division (703) 308-1202  
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